



RSL10-002GEVB: Radio SoC evaluation board, version 1 (QFN based)

The RSL10 development board is used to easily develop Bluetooth® low energy technology-enabled applications based on the industry's lowest power radio System-on-Chip (SoC).

Key Features:

- Compliance with the Arduino form factor
- Support for PMOD (e.g., J4 is a standard connector)
- On-board J-Link feature for simple debugging
- Alternate on-board SWJ-DP (serial-wire and/or JTAG) for ARM® Cortex® -M3 processor debugging
- Access to all RSL10 peripherals via standard 0.1" headers
- On-board 4-bit level translator to translate the LPDSP32 debug interface at a low voltage to a 3.3 V JTAG debugger
- Antenna matching and filtering network
- Integrated PCB antenna



Evaluation/Development Tool Information				
Product	Status	Compliance	Short Description	Parts Used
RSL10-002GEVB	Active	Pb-free	Radio SoC evaluation board, version 1 (QFN based)	NCH-RSL10-101Q48-ABG